



Verification and Validation Standards

for the

<Program Name>

Document No: <Doc Number>

Revision: -

<Name>, Program Manager

Date

<Name>, Technical Project Lead

Date

<Name>, Engineer

Date

<Name>, Quality Engineer

Date

Notice

This document and the information contained herein are the property of <Company Name>. Any reproduction, disclosure or use thereof is prohibited except as authorized in writing by <Company Name>. Recipient accepts the responsibility for maintaining the confidentiality of the contents of this document.

Table of Contents

Section	Page
1.0 INTRODUCTION	4
1.1 Purpose.....	4
1.2 Scope	4
1.3 Acronyms and Abbreviations	5
1.4 Applicable Documents	6
1.4.1 <i>External Documents</i>	6
1.4.2 <i>Internal Documents</i>	6
2.0 VALIDATION GUIDELINES	7
3.0 VERIFICATION GUIDELINES	9
3.1 Functional Test.....	9
3.2 HDL Simulation	9
3.3 In-Circuit Test.....	9
3.4 Analysis	9
APPENDIX A: HARDWARE PLANNING REVIEW CHECKLIST	10
APPENDIX B: HARDWARE REQUIREMENTS REVIEW CHECKLIST	12
APPENDIX C: HARDWARE PRELIMINARY DESIGN REVIEW CHECKLIST	14
APPENDIX D: HARDWARE CRITICAL DESIGN REVIEW CHECKLIST	15
APPENDIX E: HARDWARE IMPLEMENTATION REVIEW CHECKLIST	16
APPENDIX F: HARDWARE INTEGRATION REVIEW CHECKLIST	17
APPENDIX G: HARDWARE VERIFICATION REVIEW CHECKLIST	18
Appendix G1: Functional Test Standard	21
Appendix G2: HDL Simulation Test Standard	22
Appendix G3: In-Circuit Test Standard	23
Appendix G4: Analysis Standard	24
APPENDIX H: HARDWARE CONFORMITY REVIEW CHECKLIST	25